Thyristors BT152 series

GENERAL DESCRIPTION

Glass passivated thyristors in a plastic envelope, intended for use in applications requiring high bidirectional blocking voltage capability and high thermal cycling performance. Typical applications include motor control, industrial and domestic lighting, heating and static switching.

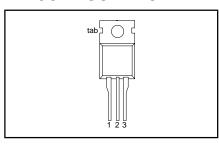
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
	BT152-	400R	600R	800R	
$V_{\mathrm{DRM}}, V_{\mathrm{RRM}}$	Repetitive peak off-state voltages	450	650	800	V
I _{T(AV)}	Average on-state current	13	13	13	Ą
I _{T(RMS)} I _{TSM}	RMS on-state current Non-repetitive peak on-state current	20 200	20 200	20 200	A A

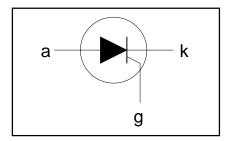
PINNING - TO220AB

PIN	DESCRIPTION			
1	cathode			
2	anode			
3	gate			
tab	anode			

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.		MAX.		UNIT
V_{DRM}	Repetitive peak off-state voltages		-	-400R 450 ¹	-600R 650 ¹	-800R 800	V
I _{T(AV)} I _{T(RMS)} I _{TSM}	Average on-state current RMS on-state current Non-repetitive peak on-state current	half sine wave; $T_{mb} \le 103$ °C all conduction angles half sine wave; $T_j = 25$ °C prior to surge	-		13 20		A A
		t = 10 ms t = 8.3 ms	-		200 220		A A
l ² t	I ² t for fusing	t = 10 ms	-		200		A ² s
dl _⊤ /dt	Repetitive rate of rise of on-state current after triggering	$I_{TM} = 50 \text{ A}; I_G = 0.2 \text{ A}; \\ dI_G/dt = 0.2 \text{ A}/\mu\text{s}$	-		200		A/μs
I _{GM}	Peak gate current		-		5		Α
V_{GM}	Peak gate voltage		-		5 5		V
V _{RGM}	Peak reverse gate voltage		-				V W
P_{GM}	Peak gate power Average gate power	over any 20 ms period	_		20 0.5		W
$ \begin{array}{c} P_{G(AV)} \\ T_{stg} \\ T_{j} \end{array} $	Storage temperature Operating junction temperature	Tover any 20 ms penou	-40 -		150 125		ာ့် လို့

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¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the thyristor may switch to the on-state. The rate of rise of current should not exceed 15 A/ μ s.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Thermal resistance		-	-	1.1	K/W
R _{th i-a}	junction to mounting base Thermal resistance junction to ambient	in free air	-	60	-	K/W

STATIC CHARACTERISTICS

 $T_j = 25$ °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{GT}	Gate trigger current	$V_D = 12 \text{ V}; I_T = 0.1 \text{ A}$		3	32	mA
l IĽ	Latching current	$V_{\rm D} = 12 \text{ V}; I_{\rm GT} = 0.1 \text{ A}$	-	25	80	mΑ
I _H	Holding current	$V_D = 12 \text{ V}; I_{GT} = 0.1 \text{ A}$	-	15	60	mΑ
V _T	On-state voltage	$I_{T} = 40 \text{ A}$	-	1.4	1.75	V
V _{GT}	Gate trigger voltage	$\dot{V}_{D} = 12 \text{ V}; I_{T} = 0.1 \text{ A}$	-	0.6	1.5	V
		$V_D = V_{DRM(max)}$, $I_T = 0.1 \text{ A}$; $T_j = 125 ^{\circ}\text{C}$	0.25	0.4	-	V
I_D, I_R	Off-state leakage current	$V_D = V_{DRM(max)}$; $V_R = V_{RRM(max)}$; $T_j = 125 ^{\circ}C$	-	0.2	1.0	mA

DYNAMIC CHARACTERISTICS

T_i = 25 °C unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV _D /dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}; T_j = 125 °C;$ exponential waveform gate open circuit	200	300	-	V/μs
t _{gt}	Gate controlled turn-on	$V_D = V_{DRM(max)}$; $I_G = 0.1$ Å; $dI_G/dt = 5$ A/ μ s; $I_{TM} = 40$ Å	-	2	-	μs
t _q	Circuit commutated turn-off time	$V_D^{''}=67\% \ V_{DRM(max)}; \ T_j=125\ ^{\circ}C; \ I_{TM}=50\ A; \ V_R=25\ V; \ dI_{TM}/dt=30\ A/\mu s; \ dV_D/dt=50\ V/\mu s; \ R_{GK}=100\ \Omega$	-	70	-	μs

Thyristors BT152 series

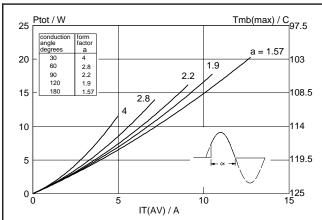


Fig.1. Maximum on-state dissipation, P_{tot} , versus average on-state current, $I_{T(AV)}$, where $a = form\ factor = I_{T(RMS)}/I_{T(AV)}$.

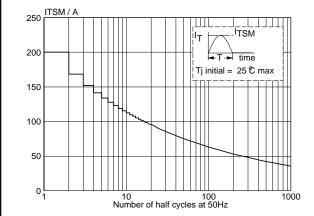


Fig.4. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, f = 50 Hz.

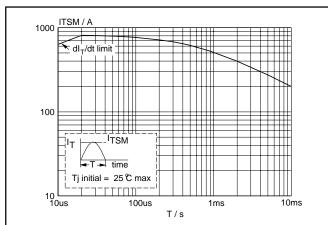


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \le 10$ ms.

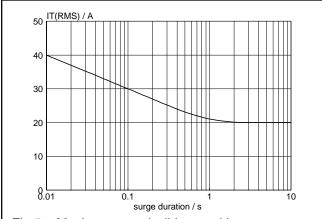


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, f = 50 Hz; $T_{mb} \le 103$ °C.

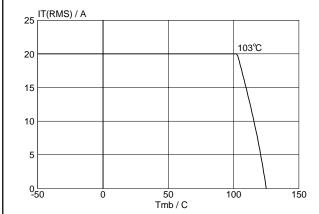
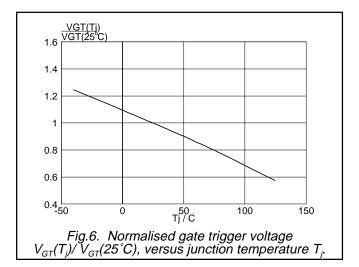
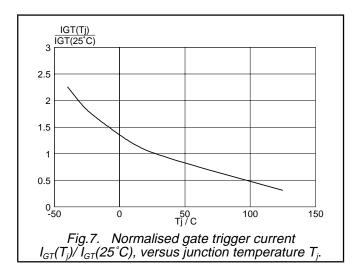
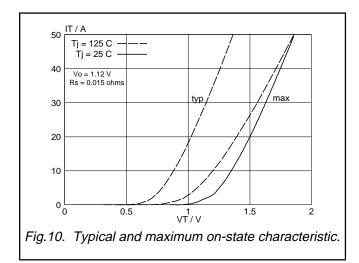


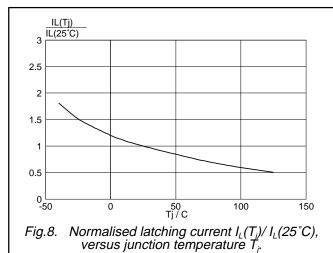
Fig.3. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

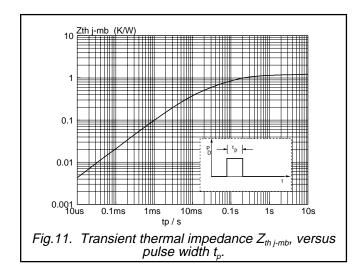


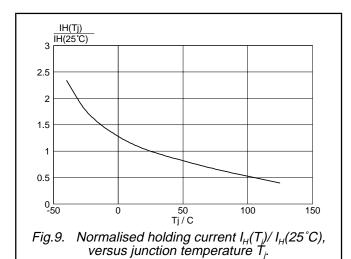
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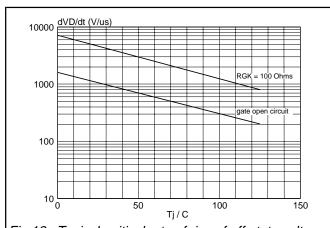
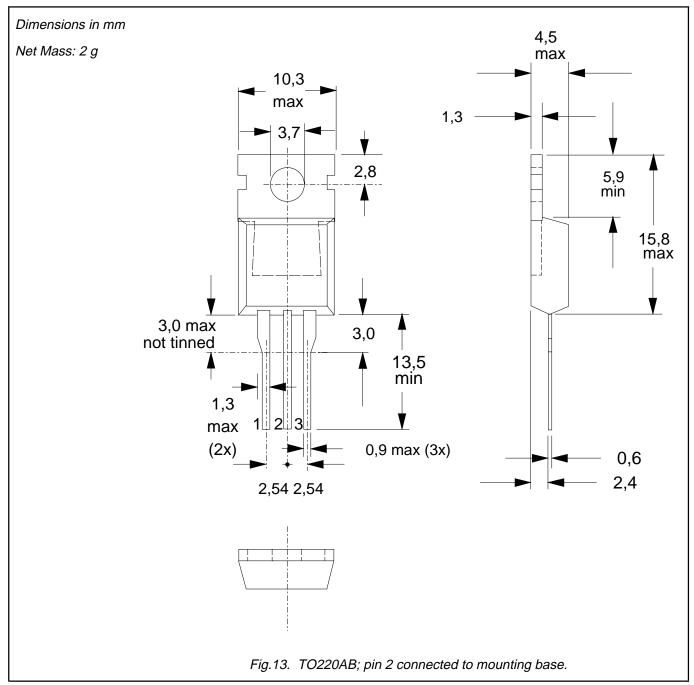


Fig.12. Typical, critical rate of rise of off-state voltage, dV_D/dt versus junction temperature T_j.

BT152 series **Thyristors**

MECHANICAL DATA



- Notes
 1. Refer to mounting instructions for TO220 envelopes.
 2. Epoxy meets UL94 V0 at 1/8".

Thyristors BT152 series

DEFINITIONS

Data sheet status						
This data sheet contains target or goal specifications for product development.						
This data sheet contains preliminary data; supplementary data may be published later.						
This data sheet contains final product specifications.						
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Limiting values

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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