

MOS FIELD EFFECT TRANSISTOR 2SK3053

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK3053 is N-Channel MOS Field Effect Transistor designed for high current switching applications in consumer instruments.

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3053	Isolated TO-220

FEATURES

• Low On-State Resistance

 $R_{DS(on)1} = 45 \text{ m}\Omega \text{ MAX. (Vgs} = 10 \text{ V, ID} = 13 \text{ A)}$

 $R_{DS(on)2} = 70 \text{ m}\Omega$ MAX. (Vgs = 4.0 V, ID = 13 A)

- Low Ciss: Ciss = 790 pF TYP.
- · Built-in Gate Protection Diode
- Isolated TO-220 package

(Isolated TO-220)



ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Drain to Source Voltage	Voss	60	V
Gate to Source Voltage	VGSS(AC)	±20	V
Gate to Source Voltage	VGSS(DC)	+20, -10	V
Drain Current (DC)	I _{D(DC)}	±25	Α
Drain Current (Pulse) Note1	D(pulse)	±75	Α
Total Power Dissipation (Tc = 25°C)	PT	30	W
Total Power Dissipation (T _A = 25°C)	PT	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	T _{stg}	-55 to +150	°C
Single Avalanche Current Note2	IAS	12.5	Α
Single Avalanche Energy Note2	Eas	15.6	mJ

Notes 1. PW \leq 10 μ s, Duty cycle \leq 1 %

2. Starting T_{ch} = 25 °C, V_{DD} = 30 V, R_G = 25 Ω , V_{GS} = 20 V \rightarrow 0 V

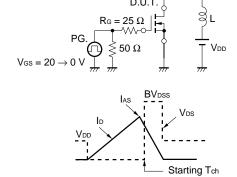
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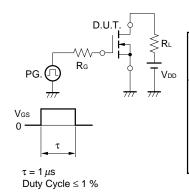
ELECTRICAL CHARACTERISTICS (TA = 25 °C)

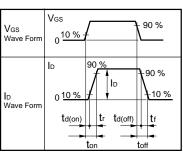
	CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, ID = 13 A		28	45	mΩ
		RDS(on)2	Vgs = 4.0 V, ID = 13 A		46	70	mΩ
	Gate to Source Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.0	1.6	2.0	V
	Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 13 A	8.0	16		S
	Drain Leakage Current	IDSS	V _{DS} = 60 V, V _{GS} = 0 V			10	μΑ
	Gate to Source Leakage Current	Igss	Vgs = ±20 V, Vps = 0 V			±10	μΑ
	Input Capacitance	Ciss	V _{DS} = 10 V		790		pF
	Output Capacitance	Coss	V _G s = 0 V		240		pF
	Reverse Transfer Capacitance	Crss	f = 1 MHz		100		pF
	Turn-on Delay Time	td(on)	I _D = 13 A		20		ns
*	Rise Time	tr	V _G S = 10 V		200		ns
	Turn-off Delay Time	td(off)	VDD = 30 V		65		ns
	Fall Time	t f	R _G = 10 Ω		95		ns
	Total Gate Charge	Q G	ID = 25 A		20		nC
	Gate to Source Charge	Qgs	V _{DD} = 48 V		3.0		nC
*	Gate to Drain Charge	Q _{GD}	V _{GS} = 10 V		6.5		nC
	Body Diode Forward Voltage	V _{F(S-D)}	IF = 25 A, VGS = 0 V		1.0		V
	Reverse Recovery Time	trr	IF = 25 A, VGS = 0 V		40		ns
	Reverse Recovery Charge	Qrr	di/dt = 100 A/μs		45		nC

TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME



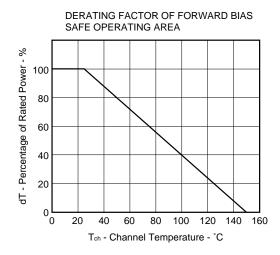


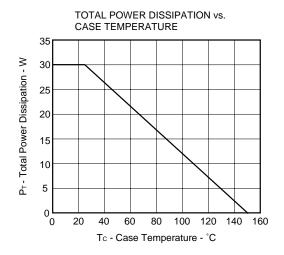


TEST CIRCUIT 3 GATE CHARGE

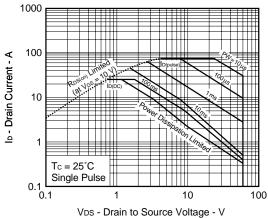
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TYPICAL CHARACTERISTICS (TA = 25 °C)

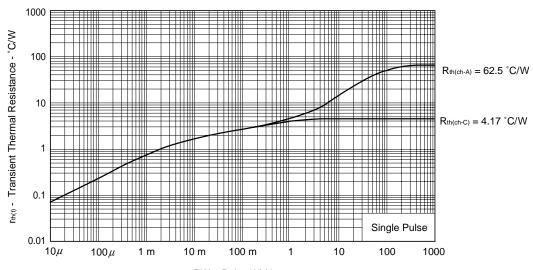




★ FORWARD BIAS SAFE OPERATING AREA



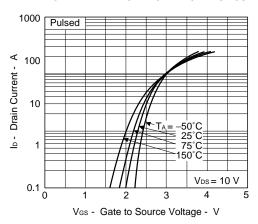
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



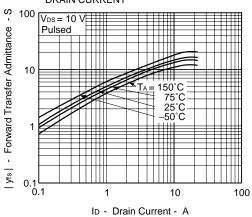
PW - Pulse Width - s

3

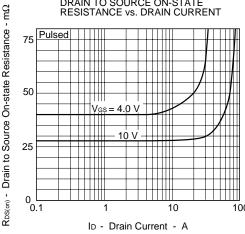
FORWARD TRANSFER CHARACTERISTICS



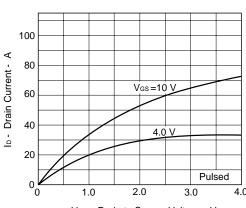
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

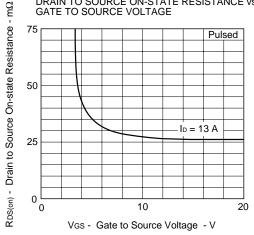


DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

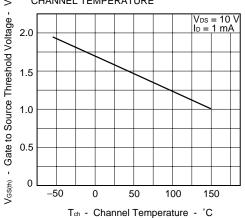


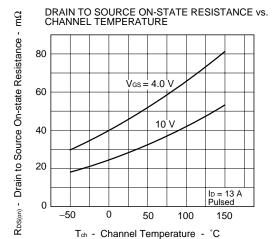
V_{DS} - Drain to Source Voltage - V

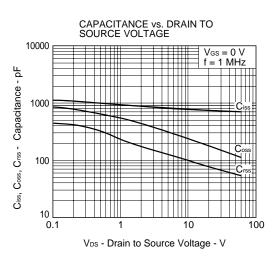
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

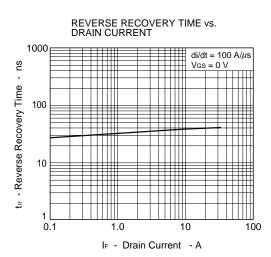


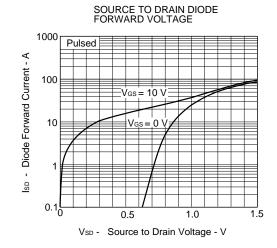
GATE TO SOURCE THRESHOLD VOLTAGE vs. CHANNEL TEMPERATURE

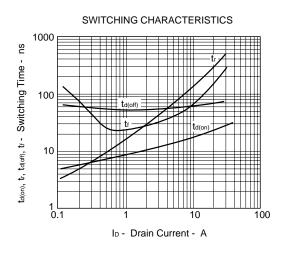


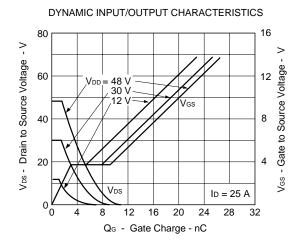


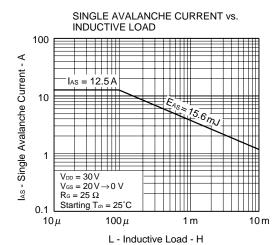


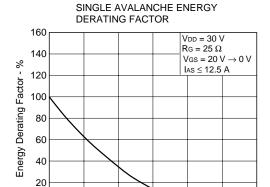












100 Starting T $_{\text{ch}}$ - Starting Channel Temperature - $^{\circ}\text{C}$

125

150

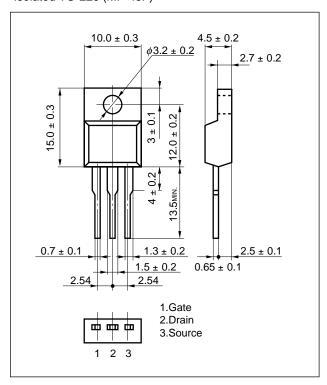
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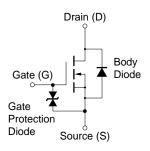
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PACKAGE DRAWING

Isolated TO-220 (MP-45F)



EQUIVALENT CIRCUIT



Remark 1. This product is designed for consumer application and isn't suitable for automotive application.

2. The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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