



**Product data sheet** 

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## 1. Product profile

#### 1.1 General description

Passivated sensitive gate thyristor in a SOT54 plastic package.

#### 1.2 Features

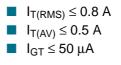
Designed to be interfaced directly to microcontrollers, logic integrated circuits and other low power gate trigger circuits

#### **1.3 Applications**

General purpose switching and phase control

#### 1.4 Quick reference data

- $V_{DRM} \le 400 V$
- $\blacksquare V_{RRM} \le 400 V$
- $\blacksquare I_{TSM} \le 8 \text{ A}$



## 2. Pinning information

Pin	Description	Simplified outline	Symbol
1	anode (A)		<b>N</b> 1
2	gate (G)		А Ӈ К
3	cathode (K)		G sym037
		SOT54 (TO-92)	



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## 3. Ordering information

w.datasheet4u.com Table 2. Ordering information					
	Type number	Package			
		Name	Description	Version	
	BT169D-L	TO-92	plastic single-ended leaded (through hole) package; 3 leads	SOT54	

## 4. Limiting values

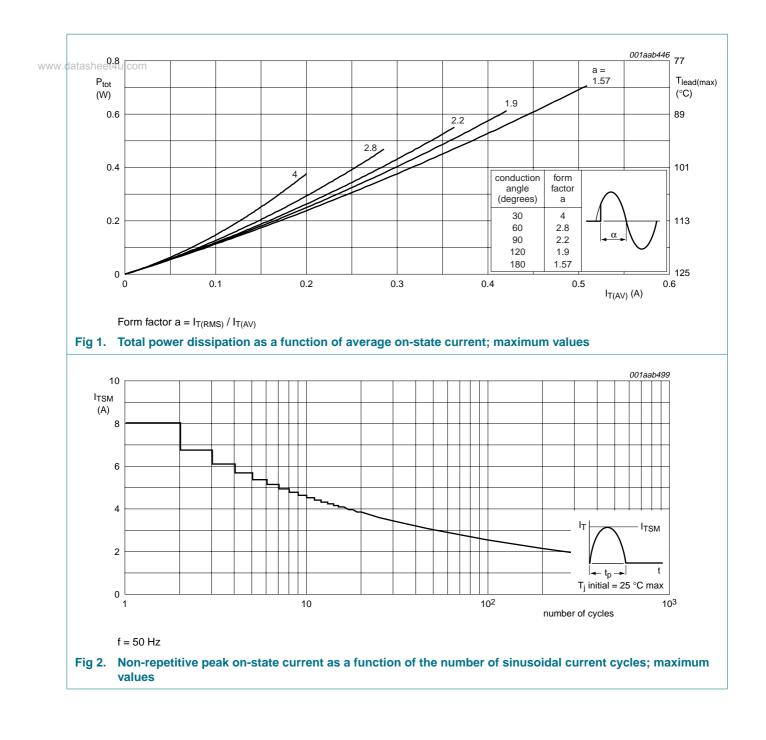
#### Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DRM</sub>	repetitive peak off-state voltage		<u>[1]</u> _	400	V
V <sub>RRM</sub>	repetitive peak reverse voltage		<u>[1]</u> _	400	V
$I_{T(AV)}$	average on-state current	half sine wave; T <sub>lead</sub> ≤ 83 °C; see <u>Figure 1</u>	-	0.5	A
I <sub>T(RMS)</sub>	RMS on-state current	all conduction angles; see Figure 4 and $\underline{5}$	-	0.8	A
I <sub>TSM</sub>	non-repetitive peak on-state current	half sine wave; $T_j = 25 \text{ °C}$ prior to surge; see Figure 2 and 3			
		t = 10 ms	-	8	А
		t = 8.3 ms	-	9	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	t = 10 ms	-	0.32	A <sup>2</sup> s
dl <sub>T</sub> /dt	rate of rise of on-state current	$I_{TM}$ = 2 A; $I_G$ = 10 mA; d $I_G$ /dt = 100 mA/µs	-	50	A/μs
I <sub>GM</sub>	peak gate current		-	1	А
V <sub>GM</sub>	peak gate voltage		-	5	V
V <sub>RGM</sub>	peak reverse gate voltage		-	5	V
P <sub>GM</sub>	peak gate power		-	2	W
P <sub>G(AV)</sub>	average gate power	over any 20 ms period	-	0.1	W
T <sub>stg</sub>	storage temperature		-40	+150	°C
Tj	junction temperature		-	125	°C

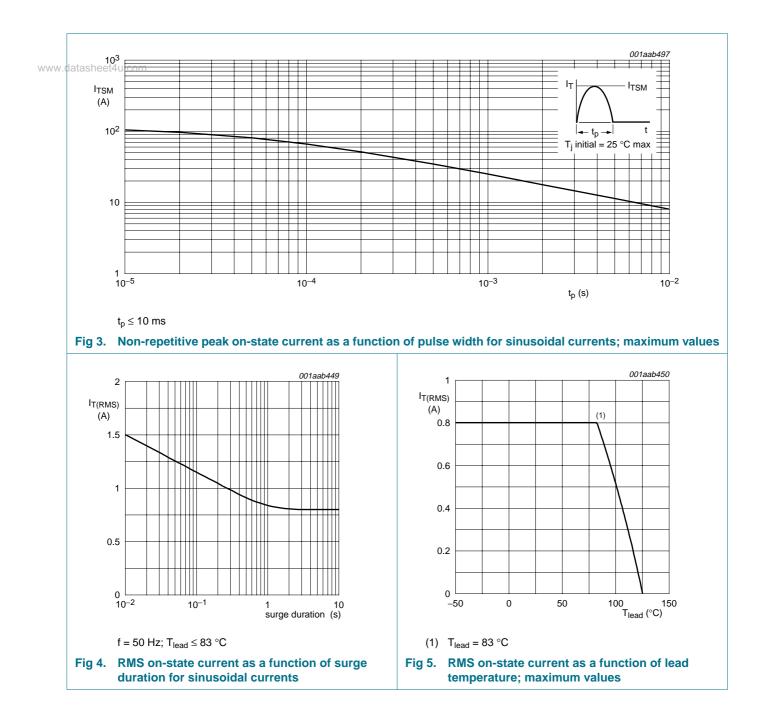
[1] Although not recommended, off-state voltages up to 800 V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/µs.

## BT169D-L Thyristor, logic level



# BT169D-L

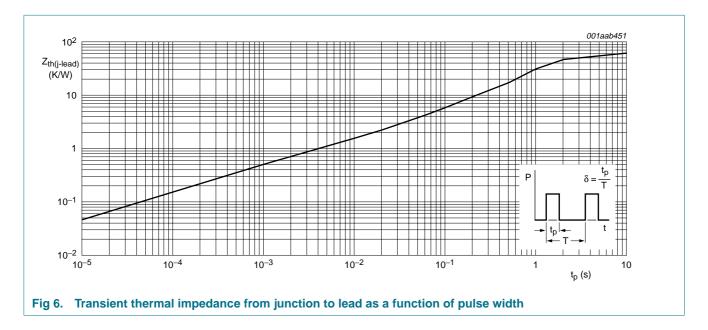
Thyristor, logic level



## 5. Thermal characteristics

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Table 4.	Thermal characteristics						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
$R_{th(j-lead)}$	thermal resistance from junction to lead	see Figure 6	-	-	60	K/W	
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	Printed-circuit board mounted; lead length = 4 mm	-	150	-	K/W	

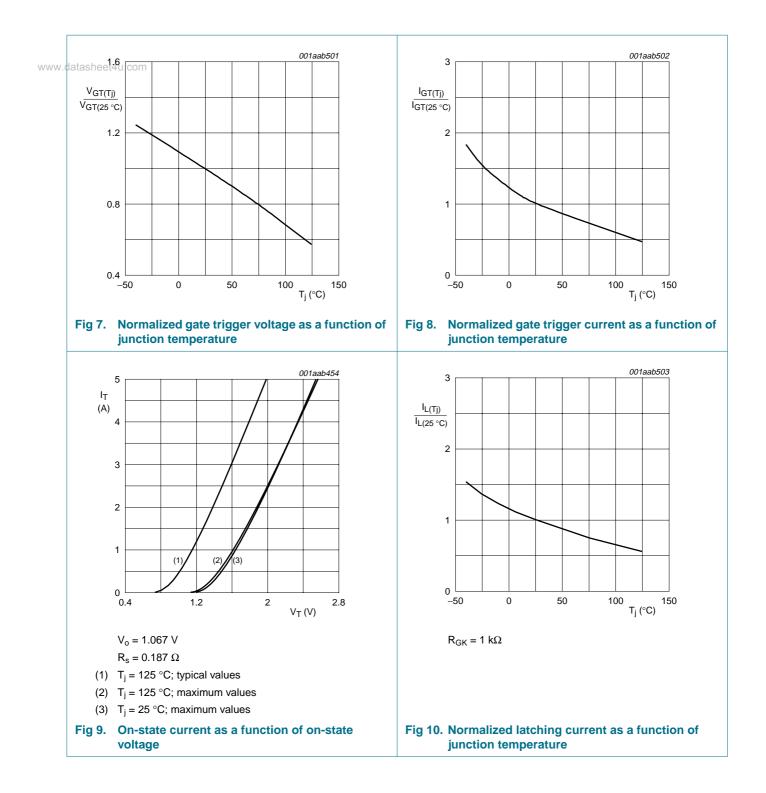


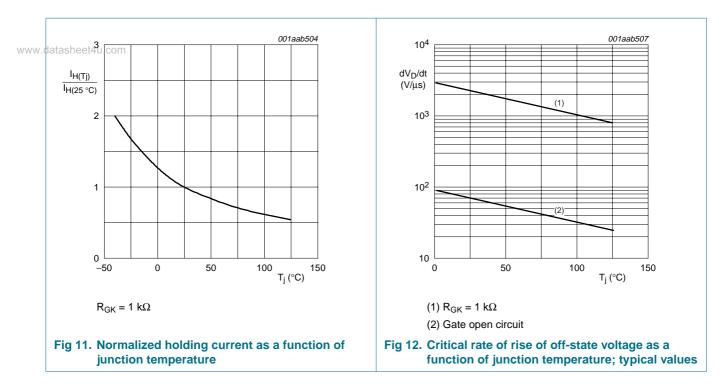
## 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Un
Static cha	racteristics					
I <sub>GT</sub>	gate trigger current	$V_D = 12 \text{ V}; I_T = 10 \text{ mA};$ gate open circuit; see Figure 8	-	-	50	μA
IL	latching current	$V_D$ = 12 V; I <sub>GT</sub> = 0.5 mA; R <sub>GK</sub> = 1 k $\Omega$ ; see <u>Figure 10</u>	-	2	6	mA
I <sub>H</sub>	holding current	$V_D$ = 12 V; $I_{GT}$ = 0.5 mA; $R_{GK}$ = 1 k\Omega; see $\underline{Figure~11}$	-	2	5	mA
V <sub>T</sub>	on-state voltage	I <sub>T</sub> = 1.2 A	-	1.25	1.7	V
V <sub>GT</sub>	gate trigger voltage	I <sub>T</sub> = 10 mA; gate open circuit; see <u>Figure 7</u>				
		V <sub>D</sub> = 12 V	-	0.5	0.8	V
		$V_D = V_{DRM(max)}; T_j = 125 \ ^{\circ}C$	0.2	0.3	-	V
I <sub>D</sub>	off-state current	$V_D = V_{DRM(max)}; T_j = 125 \ ^\circC;$ $R_{GK} = 1 \ k\Omega$	-	0.05	0.1	mA
Dynamic o	characteristics					
dV <sub>D</sub> /dt	rate of rise of off-state voltage	$V_{DM} = 0.67 \times V_{DRM(max)}$ ; T <sub>j</sub> = 125 °C; exponential waveform; see Figure 12				
		$R_{GK} = 1 \ k\Omega$	500	800	-	V/µ
		gate open circuit	-	25	-	V/ļ
t <sub>gt</sub>	gate-controlled turn-on time	$\begin{split} I_{TM} = 2 \text{ A};  V_D = V_{DRM(max)};  I_G = 10  \text{mA}; \\ dI_G/dt = 0.1  A/\mu s \end{split}$	-	2	-	μs
tq	commutated turn-off time	$ \begin{split} &V_{DM} = 0.67 \times V_{DRM(max)}; \ T_{j} = 125 \ ^{\circ}C; \\ &I_{TM} = 1.6 \ A; \ V_{R} = 35 \ V; \\ &(dI_{T}/dt)_{M} = 30 \ A/\mu s; \ dV_{D}/dt = 2 \ V/\mu s; \\ &R_{GK} = 1 \ k\Omega \end{split} $	-	100	-	μs

# **BT169D-L**

**Thyristor, logic level** 





## 7. Package information

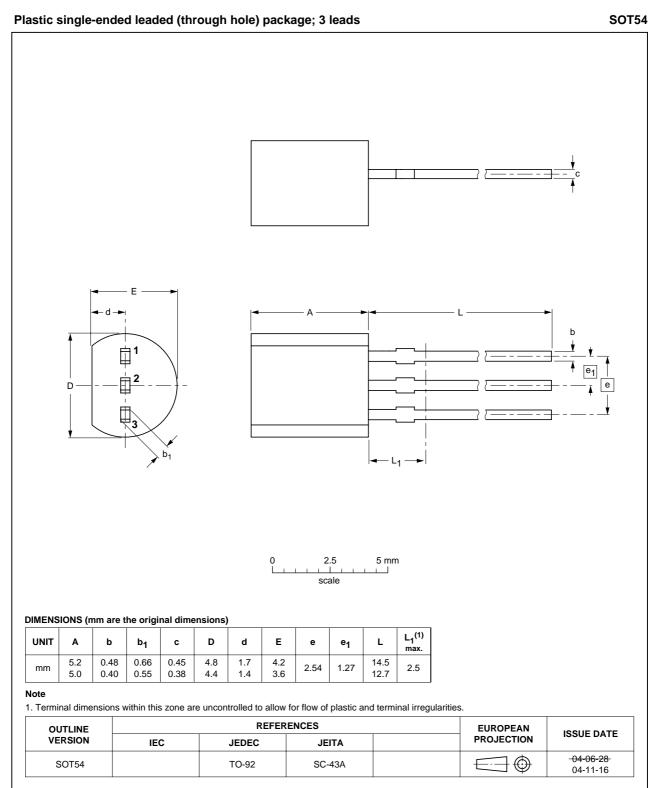
Epoxy meets requirements of UL 94 V-0 at 3.175 mm

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## 8. Package outline

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#### Fig 13. Package outline SOT54 (TO-92)

## 9. Revision history

/w.datasheet4u.com Table 6. Revision history					
Document ID	Release date	Data sheet status	Change notice	Supersedes	
BT169D-L_1	20071112	Product data sheet	-	-	

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Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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Product [short] data sheet	Production	This document contains the product specification.

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[2] The term 'short data sheet' is explained in section "Definitions".

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