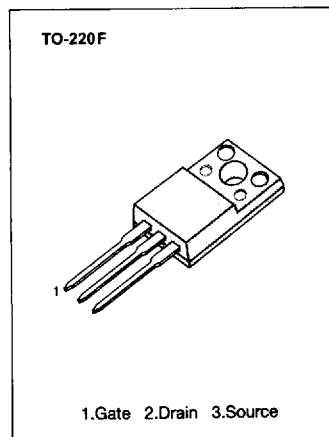


IRFS640/641**FEATURES**

- Lower $R_{DS(on)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability

PRODUCT SUMMARY

Part Number	V _{DS}	R _{DS(on)}	I _D
IRFS640	200V	0.18 Ω	9.8A
IRFS641	150V	0.18 Ω	9.8A

**ABSOLUTE MAXIMUM RATINGS**

Characteristic	Symbol	IRFS640	IRFS641	Unit
Drain-Source Voltage (1)	V _{DSS}	200	150	Vdc
Drain-Gate Voltage (R _{GS} =1.0M Ω)(1)	V _{DGR}	200	150	Vdc
Gate-Source Voltage	V _{GS}	± 20		Vdc
Continuous Drain Current T _c =25 °C	I _D	9.8		Adc
Continuous Drain Current T _c =100 °C	I _D	6.8		Adc
Drain Current - Pulsed (3)	I _{DM}	72		Adc
Gate Current - Pulsed	I _{GM}	± 1.5		Adc
Single Pulsed Avalanche Energy (4)	E _{AS}	178		mJ
Avalanche Current	I _{AS}	9.8		A
Total Power Dissipation at T _c =25 °C	P _D	40		Watts
Derate above 25 °C		0.32		W/ °C
Operating and Storage Junction Temperature Range	T _J , T _{STG}	-55 to +150		°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T _L	300		°C

Notes : (1) T_J=25°C to 150°C(2) Pulse test : Pulse width $\leq 300\mu s$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by max. junction temperature

(4) L=2.7mH, V_{dd}=50V, R_G=25 Ω , Starting T_J=25°C

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N-CHANNEL
POWER MOSFETSELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage					
	IRFS640	200	-	-	V	V _{GS} =0V, I _D =250μA
	IRFS641	150	-	-		
V _{GS(th)}	Gate Threshold Voltage	2.0	-	4.0	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V _{GS} =20V
I _{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V _{GS} =-20V
I _{DSS}	Zero Gate Voltage Drain Current	-	-	250	μA	V _{DS} =Max. Rating, V _{GS} =0V
		-	-	1000		V _{DS} =0.8 Max. Rating, V _{GS} =0V, T _c =125°C
R _{DS(on)}	Static Drain-Source On Resistance(2)	-	-	0.18	Ω	V _{GS} =10V, I _D =9.0A
g _{fs}	Forward Transconductance (2)	6.0	9.5	-	Ω	V _{DS} ≥50V, I _D =9.0A
C _{iss}	Input Capacitance	-	1400	-	pF	V _{GS} =0V, V _{DS} =25V, f=1.0MHz
C _{oss}	Output Capacitance	-	240	-	pF	
C _{rss}	Reverse Transfer Capacitance	-	95-	-	pF	
t _{d(on)}	Turn-On Delay Time	-	-	30	ns	V _{DD} =0.5 BV _{DSS} , I _D =18A, Z _o =9.1Ω (MOSFET switching times are essentially independent of operating temperature)
t _r	Rise Time	-	-	60	ns	
t _{d(off)}	Turn-Off Delay Time	-	-	80	ns	
t _f	Fall Time	-	-	60	ns	
Q _g	Total Gate Charge (Gate-Source Plus Gate-Drain)	-	-	64	nC	V _{GS} =10V, I _D =18A, V _{DS} =0.8 Max. Rating (Gate charge is essentially independent of operating temperature)
Q _{gs}	Gate-Source Charge	-	12.3	-	nC	
Q _{gd}	Gate-Drain ("Miller") Charge	-	25.3	-	nC	

THERMAL RESISTANCE

Symbol	Characteristics		All	Units	Remark
R _{thJC}	Junction-to-Case	MAX	3.12	K/W	
R _{thCS}	Case-to-Sink	TYP	0.5	K/W	Mounting surface flat, smooth and greased
R _{thJA}	Junction-to-Ambient	MAX	62.5	K/W	Free Air Operation

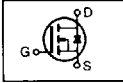
Notes : (1) T_J=25°C to 150°C

(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%

(3) Repetitive rating : Pulse width limited by max. junction temperature

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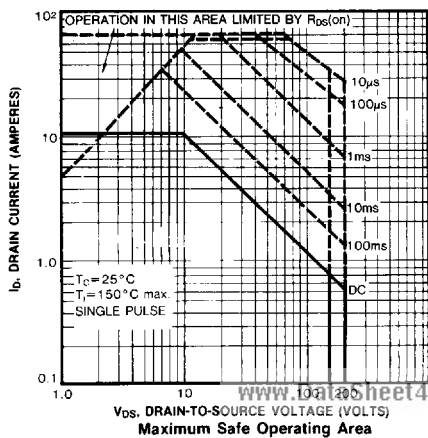
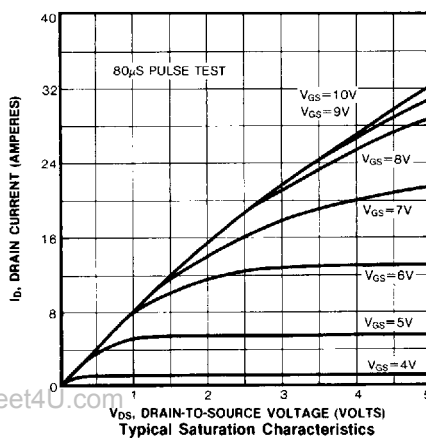
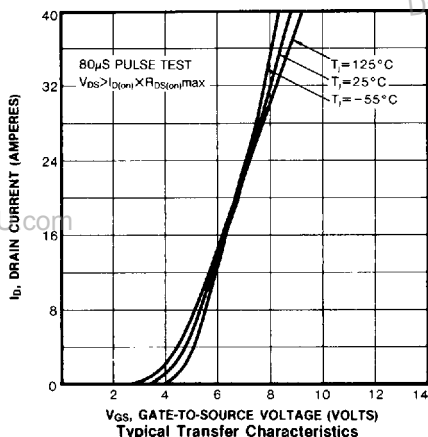
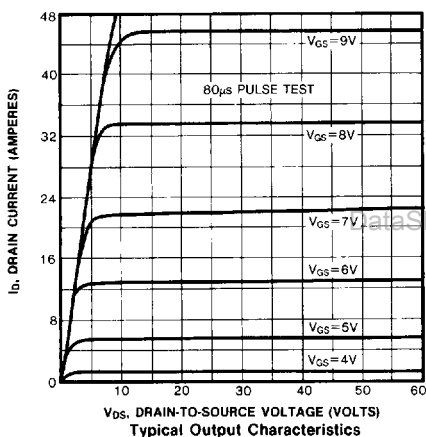
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

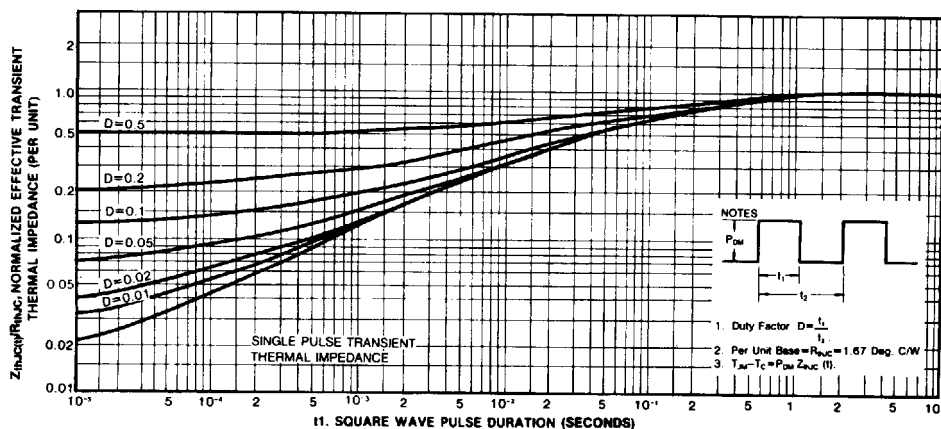
Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	-	-	18	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier 
I_{SM}	Pulse Source Current (Body Diode) (3)	-	-	72	A	
V_{SD}	Diode Forward Voltage (2)	-	-	2.0	V	$T_J=25^\circ\text{C}$, $I_S=18\text{A}$, $V_{GS}=0\text{V}$
t_{rr}	Reverse Recovery Time	-	650	-	ns	$T_J=25^\circ\text{C}$, $I_F=18\text{A}$, $dI_F/dt=100\text{A}/\mu\text{S}$

Notes : (1) $T_J=25^\circ\text{C}$ to 150°C

(2) Pulse test : Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

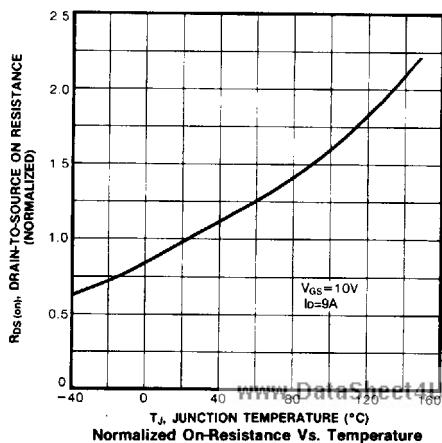
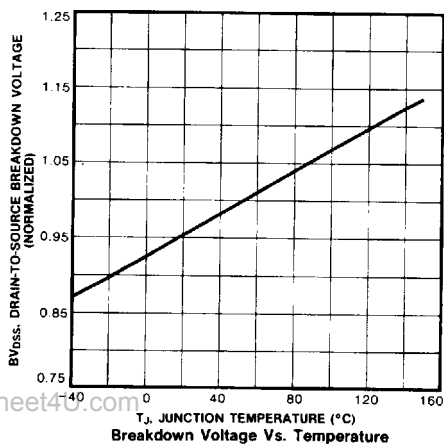
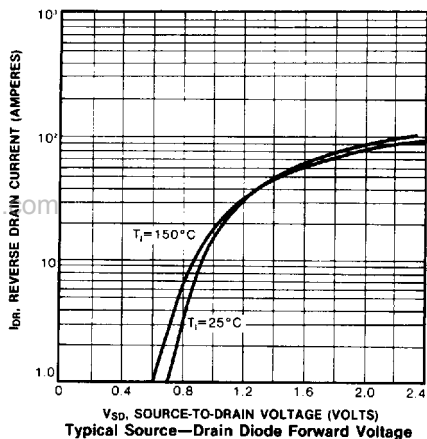
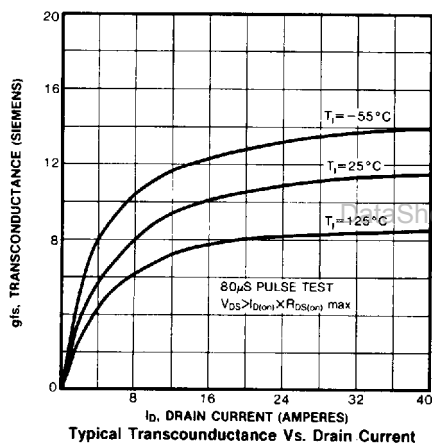
(3) Repetitive rating : Pulse width limited by max. junction temperature





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