

KSD5703

High Voltage Color Display Horizontal Deflection Output (No Damper Diode)

- High Collector-Base Voltage : V_{CBO} =1500V High Switching Speed t_F = 0.3 μ s (Max.)
- For Color TV



NPN Triple Diffused Planar Silicon Transistor

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	
V_{CBO}	Collector-Base Voltage	1500	V
V _{CEO}	Collector-Emitter Voltage	800	V
V _{EBO}	Emitter-Base Voltage	6	V
I _C	Collector Current (DC)	10	Α
I _{CP}	Collector Current (Pulse)	30	Α
P _C	Collector Dissipation (T _C =25°C)	70	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	- 55 ~ 150	°C

Electrical Characteristics T_C=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
I _{CES}	Collector Cut-off Current	V _{CE} = 1400V, V _{BE} =0			1	mA
I _{CBO}	Collector Cut-off Current	$V_{CB} = 800V, I_{E} = 0$			10	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 4V, I_{C} = 0$			1	mA
h _{FE1}	DC Current Gain	$V_{CE} = 5V, I_{C} = 1A$	15		40	
h _{FE2}		$V_{CE} = 5V, I_{C} = 8A$	5.3		7.3	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 8A, I_B = 1.6A$			5	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 8A, I _B = 1.6A			1.5	V
t _F	Fall Time	$V_{CC} = 200V, I_{C} = 6A$		0.1	0.3	μs
		$I_{B1} = 1.2A, I_{B2} = -2.4A$				
		$R_L = 33.3\Omega$				

Typical Characteristics

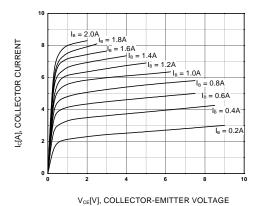


Figure 1. Static Characteristic

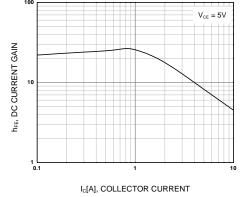


Figure 2. DC current Gain

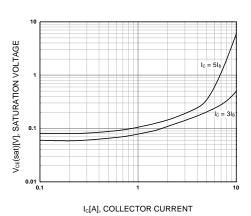


Figure 3. Collector-Emitter Saturation Voltage

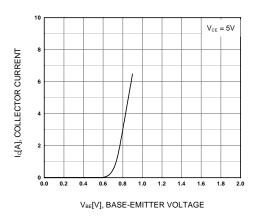


Figure 4. Base-Emitter On Voltage

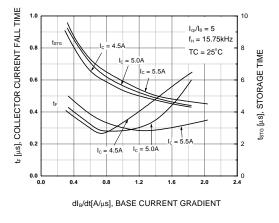


Figure 5. Switching Time

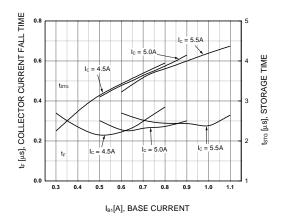
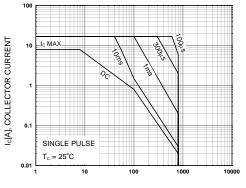


Figure 6. Switching Time

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Typical Characteristics (Continued)



 $V_{\text{CE}}[V], \, \text{COLLECTOR-EMITTER} \, \, \text{VOLTAGE}$

Figure 7. Safe Operating Area

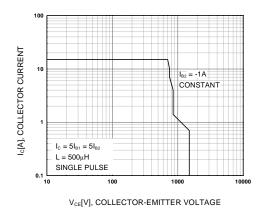


Figure 8. Reverse Bias Operating Area

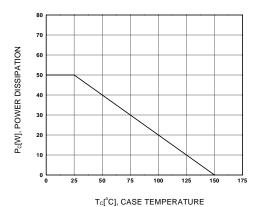
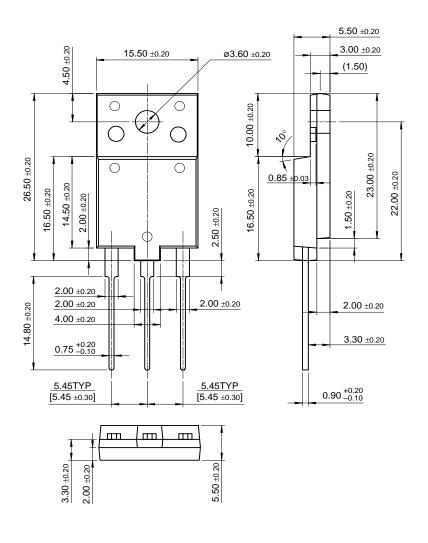


Figure 9. Power Derating

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Package Demensions

TO-3PF



Dimensions in Millimeters

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